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Information Disclosure
Statement By Applicant

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Atty Docket No.
PETAP001

Applicant:

Shah et al.

Filing Date

January 12, 2001

Application No.:

09/759,434

Group

2661

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
PN	A	5,475,682	12/12/95	Choudhury et al.			06/10/94
PN	B	5,790,522	08/04/98	Fichou et al.			06/23/95
PN	C	6,118,761	09/12/00	Kalkunte et al.			12/18/97
PN	D	6,144,635	11/07/00	Nakagawa			07/17/97
PN	E	6,636,510 B1	10/21/03	Lee et al.			05/27/99
PN	F	6,643,256 B1	11/04/03	Shimojo et al.			12/15/99
PN	G	6,721,273 B1	04/13/04	Lyon			02/03/00
PN	H	6,680,910 B1	01/20/04	Ni			06/26/98
PN	I	6,747,971 B1	06/08/04	Hughes et al.			04/20/99

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	J							
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
PN	O	Ohsaki et al., "Performance of an Input/Output Buffered-Type ATM LAN Switch with Back-Pressure Function", IEEE/ACM Transactions on Networking, Vol. 5, No. 2, April 1997.
PN	P	Prabhakar et al., "Multicast Scheduling for Input-Queued Switches", IEEE Journal on Selected Areas in Communications, May 1996.
	Q	
Examiner		Date Considered
		5/19/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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